

Fabrication of Microelectronic Devices

- The invention of the **Transistor** in 1947 was the catalyst for **Microelectronics**
- **Microelectronics** are now more abundant in our lives since **Integrated Circuit (IC)** technology became the foundation for calculators, robotics, space travel, weaponry, wrist watches, and automotive controls.
- ICs are very small, and low cost so they have allowed for manufacturers to make smaller and smaller products.
- The semiconducting material that the IC is made on is called a **Chip**, which typically hold from 10 million devices to 100 million devices, a huge improvement from 100 devices.



Microelectronic Fabrication

Richard C. Jaeger



Microelectronic Fabrication:

Introduction to Microelectronic Fabrication Richard C. Jaeger, 1988 *Plasma Electronics* Toshiaki Makabe, Zoran Lj. Petrovic, 2006-03-27 Without plasma processing techniques recent advances in microelectronics fabrication would not have been possible But beyond simply enabling new capabilities plasma based techniques hold the potential to enhance and improve many processes and applications They are viable over a wide range of size and time scales and can be used for deposition

Introduction to Microelectronic Fabrication Richard C. Jaeger, 1988 This volume is a survey of techniques in the field It is devoted to processing and is highlighted by explanations **Electron-beam Technology in Microelectronic Fabrication** J. P. Ballantyne, 1980 **The Science and Engineering of Microelectronic Fabrication** Stephen A. Campbell, 1996 The Science and Engineering of Microelectronic Fabrication provides an introduction to microelectronic processing Geared towards a wide audience it may be used as a textbook for both first year graduate and upper level undergraduate courses and as a handy reference for professionals The text covers all the basic unit processes used to fabricate integrated circuits including photolithography plasma and reactive ion etching ion implantation diffusion oxidation evaporation vapor phase epitaxial growth sputtering and chemical vapor deposition Advanced processing topics such as rapid thermal processing nonoptical lithography molecular beam epitaxy and metal organic chemical vapor deposition are also presented The physics and chemistry of each process is introduced along with descriptions of the equipment used for the manufacturing of integrated circuits The text also discusses the integration of these processes into common technologies such as CMOS double poly bipolar and GaAs MESFETs Complexity performance tradeoffs are evaluated along with a description of the current state of the art devices Each chapter includes sample problems with solutions The book also makes use of the process simulation package SUPREM to demonstrate impurity profiles of practical interest

The Science and Engineering of Microelectronic Fabrication Stephen A. Campbell, Professor in the Department of Electrical and Computer Engineering Stephen A Campbell, 2001 The Science and Engineering of Microelectronic Fabrication provides a thorough introduction to the field of microelectronic processing Geared toward a wide audience it may be used for upper level undergraduate or first year graduate courses and as a handy reference for professionals The text covers all the basic unit processes used to fabricate integrated circuits including photolithography plasma and reactive ion etching ion implantation diffusin oxidation evaporation vapor phase epitaxial growth sputtering and chemical vapor deposition Advanced processing topics such as rapid thermal processing non optical lithography molecular beam epitaxy and metal organic chemica vapor deposition are also presented The physics and chemistry of each process is introduced along with descriptions of the equipment used for the manufacturing of integrated circuits The text also discusses the itnegration of these processes into common technologies such as CMOS double poly bipolar and GaAs MESFETs Complexity performance tradeoffs are evaluated along with a description of the current state of the art devices Each chapter includes sample problems with

solutions The text makes use of the process simulation package SUPREM to demonstrate impurity profiles of practical interest The new edition includes complete chapter coverage of MEMS including Fundamentals of Mechanics Stress in Thin Films Mechanical to Electrical Transduction Mechanics of Common MEMS Devices Bulk Micromachining Etching Techniques Bulk Micromachining Process Flow Surface Micromachining Basics Surface Micromachining Process Flow MEMS Actuators High Aspect Ratio Microsystems Technology HARMST

Fabrication Engineering at the Micro and Nanoscale Stephen A. Campbell, 2008-01-10 Designed for advanced undergraduate or first year graduate courses in semiconductor or microelectronic fabrication the third edition of Fabrication Engineering at the Micro and Nanoscale provides a thorough and accessible introduction to all fields of micro and nano fabrication

Reliability and Quality in Microelectronic Manufacturing A. Christou, 2006

Electron-Beam Technology in Microelectronic Fabrication George Brewer, 2012-12-02 Electron Beam Technology in Microelectronic Fabrication presents a unified description of the technology of high resolution lithography This book is organized into six chapters each treating a major segment of the technology of high resolution lithography The book examines topics such as the physics of interaction of the electrons with the polymer resist in which the patterns are drawn the machines that generate and control the beam and ways of applying electron beam lithography in device fabrication and in the making of masks for photolithographic replication Chapter 2 discusses fundamental processes by which patterns are created in resist masks Chapter 3 describes electron beam lithography machines including some details of each of the major elements in the electron optical column and their effect on the focused electron beam Chapter 4 presents the use of electron beam lithography to make discrete devices and integrated circuits Chapter 5 looks at the techniques and economics of mask fabrication by the use of electron beams Finally Chapter 6 presents a comprehensive description and evaluation of the several high resolution replication processes currently under development This book will be of great value to students and to engineers who want to learn the unique features of high resolution lithography so that they can apply it in research development or production of the next generation of microelectronic devices and circuits

Introduction to Microfabrication Sami Franssila, 2005-01-28 Microfabrication is the key technology behind integrated circuits microsensors photonic crystals ink jet printers solar cells and flat panel displays Microsystems can be complex but the basic microstructures and processes of microfabrication are fairly simple Introduction to Microfabrication shows how the common microfabrication concepts can be applied over and over again to create devices with a wide variety of structures and functions Featuring A comprehensive presentation of basic fabrication processes An emphasis on materials and microstructures rather than device physics In depth discussion on process integration showing how processes materials and devices interact A wealth of examples of both conceptual and real devices Introduction to Microfabrication includes 250 homework problems for students to familiarise themselves with micro scale materials dimensions measurements costs and scaling trends Both research and manufacturing topics are covered with an emphasis on

silicon which is the workhorse of microfabrication This book will serve as an excellent first text for electrical engineers chemists physicists and materials scientists who wish to learn about microstructures and microfabrication techniques whether in MEMS microelectronics or emerging applications Plasma Processes for Semiconductor Fabrication W. N. G. Hitchon, 1999-01-28 An up to date description of plasma etching and deposition in semiconductor fabrication

Microelectronic Test Structures for CMOS Technology Manjul Bhushan, Mark B. Ketchen, 2011-08-26

Microelectronic Test Structures for CMOS Technology and Products addresses the basic concepts of the design of test structures for incorporation within test vehicles scribe lines and CMOS products The role of test structures in the development and monitoring of CMOS technologies and products has become ever more important with the increased cost and complexity of development and manufacturing In this timely volume IBM scientists Manjul Bhushan and Mark Ketchen emphasize high speed characterization techniques for digital CMOS circuit applications and bridging between circuit performance and characteristics of MOSFETs and other circuit elements Detailed examples are presented throughout many of which are equally applicable to other microelectronic technologies as well The authors overarching goal is to provide students and technology practitioners alike a practical guide to the disciplined design and use of test structures that give unambiguous information on the parametrics and performance of digital CMOS technology *Microchip Fabrication, 5th Ed.* Peter Van Zant, 2004-05-19 The 1 book in the industry for more than 15 years Utilizing a straightforward math free pathology this is a novice friendly guide to the semiconductor fabrication process from raw materials through shipping the finished packaged device Challenging quizzes and review summaries make this the perfect learning guide for technicians in training NEW chapter on nanotechnology NEW sections on 300mm wafer processing Processes and devices and Green processing Every chapter updated to reflect the latest processing techniques Photomask Fabrication Technology Benjamin G. Eynon, Banqiu Wu, 2005-08-11 Photomasks the printing masters for the fabrication of integrated circuits have become a necessity of modern semiconductor manufacturing This book details the science and technology of industrial photo mask production including fundamental principles industrial production flows and technological evolution **Chemical Mechanical Planarization of Microelectronic Materials** Joseph M. Steigerwald, Shyam P. Murarka, Ronald J. Gutmann, 1997 The concluding chapter describes post CMP cleaning techniques and most chapters feature problem sets to assist readers in developing a more practical understanding of CMP The only comprehensive reference to one of the fastest growing integrated circuit manufacturing technologies Chemical Mechanical Planarization of Microelectronic Materials is an important resource for research scientists and engineers working in the microelectronics industry *Plasma Electronics* Toshiaki Makabe, Zoran Lj. Petrovic, 2006-03-27 Without plasma processing techniques recent advances in microelectronics fabrication would not have been possible But beyond simply enabling new capabilities plasma based techniques hold the potential to enhance and improve many processes and applications They are viable over a wide range of size and time scales

and can be used for deposition etching and even process monitoring and diagnosis Plasma Electronics Applications in Microelectronic Device Fabrication explains the fundamental physics and numerical methods necessary to bring these technologies from the laboratory to the factory Beginning with an overview of the basic characteristics and applications of low temperature plasma preeminent experts Makabe and Petrovic explore the physics underlying the complex behavior of non equilibrium or low temperature plasma They discuss charged particle transport in general and in detail as well as macroscopic plasma characteristics and elementary processes in gas phase and on surfaces After laying this groundwork the book examines state of the art computational methods for modeling plasma and reviews various important applications including inductively and capacitively coupled plasma magnetically enhanced plasma and various processing techniques while numerous problems and worked examples reinforce the concepts Uniquely combining physics numerical methods and practical applications Plasma Electronics Applications in Microelectronic Device Fabrication equips you with the knowledge necessary to scale up lab bench breakthroughs into industrial innovations

Fabrication and Design of Resonant Microdevices Behraad Bahreyni, 2008-10-20 This book discusses the main issues of fabrication and design and applications of micromachined resonant devices including techniques commonly used for processing the output signal of resonant micro electro mechanical systems MEMS Concepts of resonance are introduced with an overview of fabrication techniques for micromachined devices important to understand as design options will depend on how the device will be fabricated Also explained excitation and signal detection methods an analytic model of device behavior a valuable design tool numerical simulation techniques issues of damping and noise for resonant MEMS electronic interfacing packaging issues and numerous examples of resonant MEMS from academia and industry Offers numerous academic and industrial examples of resonant MEMS Provides an analytic model of device behaviour Explains two port systems in detail Devotes ample space to excitation and signal detection methods Covers issues of damping and noise for resonant MEMS two topics of particular importance for high Q devices

Fundamental Principles of Optical Lithography Chris Mack, 2008-03-11 The fabrication of an integrated circuit requires a variety of physical and chemical processes to be performed on a semiconductor substrate In general these processes fall into three categories film deposition patterning and semiconductor doping Films of both conductors and insulators are used to connect and isolate transistors and their components By creating structures of these various components millions of transistors can be built and wired together to form the complex circuitry of modern microelectronic devices Fundamental to all of these processes is lithography ie the formation of three dimensional relief images on the substrate for subsequent transfer of the pattern to the substrate This book presents a complete theoretical and practical treatment of the topic of lithography for both students and researchers It comprises ten detailed chapters plus three appendices with problems provided at the end of each chapter Additional Information Visiting <http://www.lithoguru.com> textbook index.html enhances the reader's understanding as the website supplies information on how you can download a

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Microfabrication for Microfluidics Sang-Joon John Lee, Narayanan Sundararajan, 2010 Providing a definitive source of knowledge about the principles materials and process techniques used in the fabrication of microfluidics this practical volume is a must for your reference shelf The book focuses on fabrication but also covers the basic purpose benefits and limitations of the fabricated structures as they are applied to microfluidic sensor and actuator functions You find guidance on rapidly assessing options and tradeoffs for the selection of a fabrication method with clear tabulated process comparisons

Microelectronics Processing Dennis W. Hess, Klavs F. Jensen, 1989 Although chemical engineering principles are at the heart of solid state process technology until now no reference volume addressing this relationship was available This is the first book of its kind to tie fundamental engineering concepts to solid state process technology Discussing the basic concepts involved liquid phase epitaxy physical and chemical vapor deposition diffusion and oxidation in silicon resists in microlithography etc this volume will be particularly useful in chemical engineering courses It offers a framework within which specialized courses in microelectronics processing can be organized In addition it serves as a valuable reference source for all industrial engineers working with the individual process steps covered

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