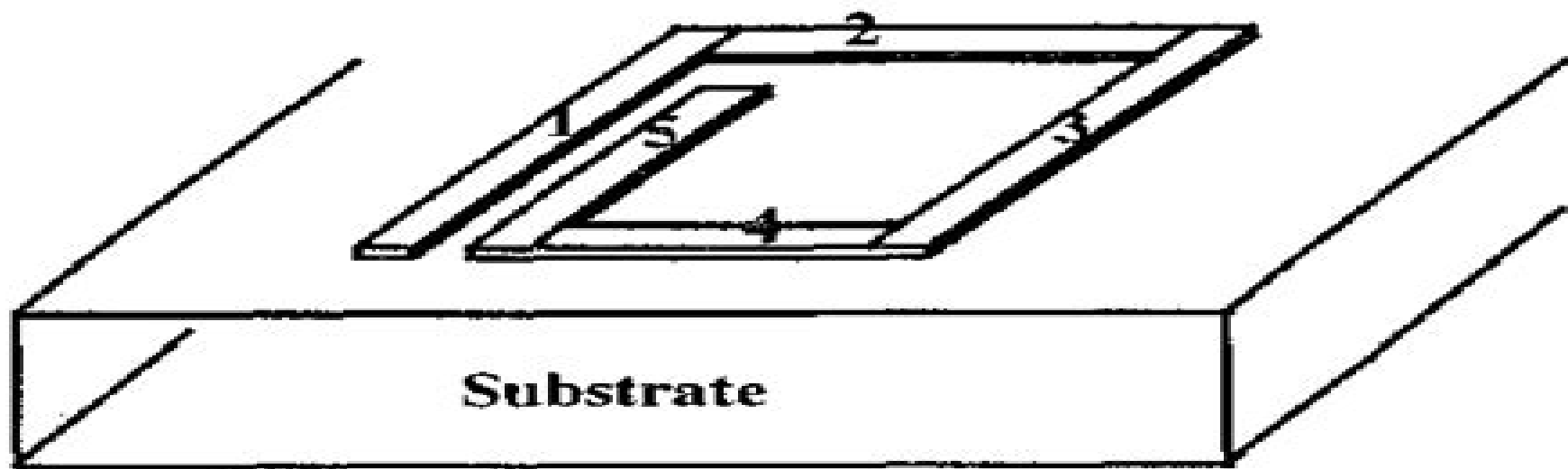
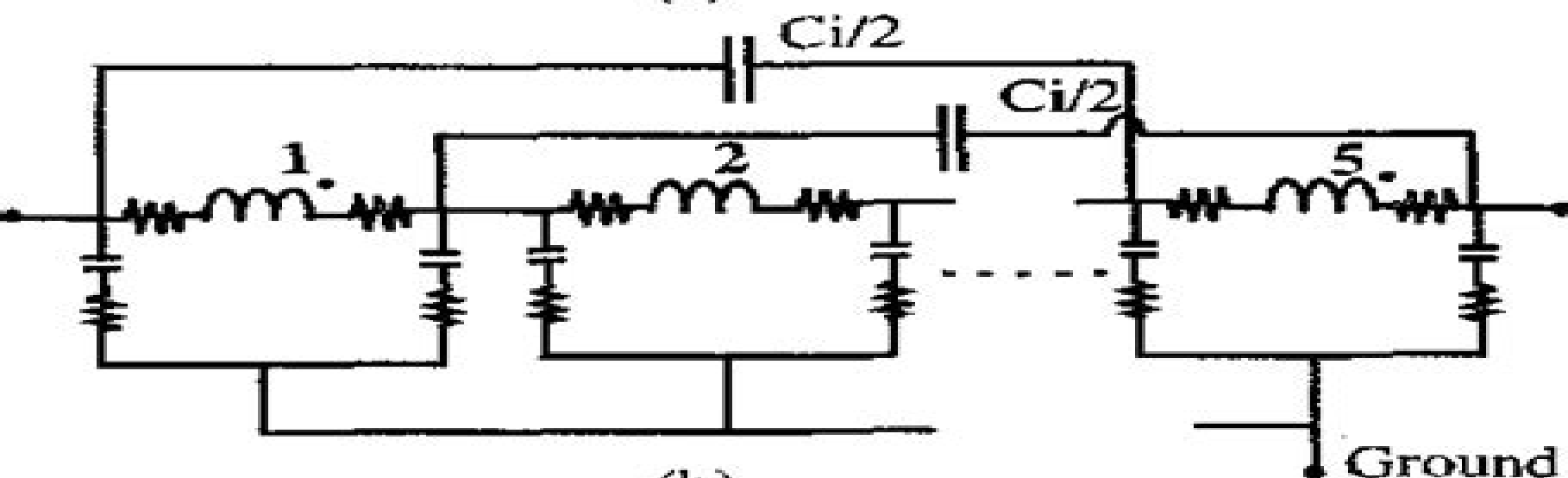


$$f_{max} = \frac{1}{2\pi\sqrt{LC_1}}$$

(2)



(a)



(b)

Figure 1. (a) A five segment inductor and (b) the complete equivalent circuit used to model it.

Parasitic-aware Optimization Of Cmos Rf Circuits

Esteban Tlelo-Cuautle



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Parasitic-Aware Optimization of CMOS RF Circuits David J. Allstot, Jinho Park, Kiyong Choi, 2005-12-02 In the arena of Parasitic Aware Design of CMOS RF Circuits efforts are aimed at the realization of true single chip radios with few if any off chip components Ironically the on chip passive components required for RF integration pose more serious challenges to SOC integration than the active CMOS and BJT devices This is not surprising since modern digital IC designs are dominated as much or more by interconnectg characteristics than by active device properties In any event the co integration of active and passive devices in RFIC design represents a serious design problem and an even more daunting manufacturing challenge If conventional mixed signal design techniques are employed parasitics associated with passive elements resistors capacitors inductors transformers pads etc and the package effectively de tune RF circuits rendering them sub optimal or virtually useless Hence dealing with parasitics in an effective way as part of the design process is an essential emerging methodology in modern SOC design The parasitic aware RF circuit synthesis techniques described in this book effectively address this critical problem Analog/RF and Mixed-Signal Circuit Systematic Design Mourad Fakhfakh, Esteban Tlelo-Cuautle, Rafael Castro-Lopez, 2013-02-03 Despite the fact that in the digital domain designers can take full benefits of IPs and design automation tools to synthesize and design very complex systems the analog designers task is still considered as a handcraft cumbersome and very time consuming process Thus tremendous efforts are being deployed to develop new design methodologies in the analog RF and mixed signal domains This book collects 16 state of the art contributions devoted to the topic of systematic design of analog RF and mixed signal circuits Divided in the two parts Methodologies and Techniques recent theories synthesis techniques and design methodologies as well as new sizing approaches in the field of robust analog and mixed signal design automation are presented for researchers and R D engineers Integrated Circuits for Analog Signal Processing Esteban Tlelo-Cuautle, 2012-07-27 This book presents theory design methods and novel applications for integrated circuits for analog signal processing The discussion covers a wide variety of active devices active elements and amplifiers working in voltage mode current mode and mixed mode This includes voltage operational amplifiers current operational amplifiers operational transconductance amplifiers operational transresistance amplifiers current conveyors current differencing transconductance amplifiers etc Design methods and challenges posed by nanometer technology are discussed and applications described including signal amplification filtering data acquisition systems such as neural recording sensor conditioning such as biomedical implants actuator conditioning noise generators oscillators mixers etc Presents analysis and synthesis methods to generate all circuit topologies from which the designer can select the best one for the desired application Includes design guidelines for active devices elements with low voltage and low power constraints Offers guidelines for selecting the right active devices elements in the design of linear and nonlinear circuits Discusses optimization of the active devices elements for process and manufacturing issues of nanometer technology **RF CMOS**

Power Amplifiers: Theory, Design and Implementation Mona M. Hella, Mohammed Ismail, 2005-12-27 RF CMOS Power Amplifiers Theory Design and Implementation focuses on the design procedure and the testing issues of CMOS RF power amplifiers This is the first monograph addressing RF CMOS power amplifier design for emerging wireless standards The focus on power amplifiers for short is distance wireless personal and local area networks PAN and LAN however the design techniques are also applicable to emerging wide area networks WAN infrastructure using micro or pico cell networks The book discusses CMOS power amplifier design principles and theory and describes the architectures and tradeoffs in designing linear and nonlinear power amplifiers It then details design examples of RF CMOS power amplifiers for short distance wireless applications e g Bluetooth WLAN including designs for multi standard platforms Design aspects of RF circuits in deep submicron CMOS are also discussed RF CMOS Power Amplifiers Theory Design and Implementation serves as a reference for RF IC design engineers and RD and R D managers in industry and for graduate students conducting research in wireless semiconductor IC design in general and with CMOS technology in particular

Computational Intelligence in Analog and Mixed-Signal (AMS) and Radio-Frequency (RF) Circuit Design Mourad Fakhfakh, Esteban Tlelo-Cuautle, Patrick Siarry, 2015-07-14 This book explains the application of recent advances in computational intelligence algorithms design methodologies and synthesis techniques to the design of integrated circuits and systems It highlights new biasing and sizing approaches and optimization techniques and their application to the design of high performance digital VLSI radio frequency and mixed signal circuits and systems This first of two related volumes addresses the design of analog and mixed signal AMS and radio frequency RF circuits with 17 chapters grouped into parts on analog and mixed signal applications and radio frequency design It will be of interest to practitioners and researchers in computer science and electronics engineering engaged with the design of electronic circuits

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Bo Liu, Georges Gielen, Francisco V. Fernández, 2013-08-16 Computational intelligence techniques are becoming more and more important for automated problem solving nowadays Due to the growing complexity of industrial applications and the increasingly tight time to market requirements the time available for thorough problem analysis and development of tailored solution methods is decreasing There is no doubt that this trend will continue in the foreseeable future Hence it is not surprising that robust and general automated problem solving methods with satisfactory performance are needed

Circuits at the Nanoscale Krzysztof Iniewski, 2018-10-08 Circuits for Emerging Technologies Beyond CMOS New exciting opportunities are abounding in the field of body area networks wireless communications data networking and optical imaging In response to these developments top notch international experts in industry and academia present Circuits at the Nanoscale Communications Imaging and Sensing This volume unique in both its scope and its focus addresses the state of the art in integrated circuit design in the context of emerging systems A must for anyone serious about circuit design for future technologies this book discusses emerging materials that can take system performance beyond standard CMOS These include Silicon on Insulator SOI Silicon Germanium SiGe and Indium Phosphide InP Three dimensional CMOS integration and co integration with Microelectromechanical MEMS technology and radiation sensors are described as well Topics in the book are divided into comprehensive sections on emerging design techniques mixed signal CMOS circuits circuits for communications and circuits for imaging and sensing Dr Krzysztof Iniewski is a director at CMOS Emerging Technologies Inc a consulting company in Vancouver British Columbia His current research interests are in VLSI circuits for medical applications He has published over 100 research papers in international journals and conferences and he holds 18 international patents granted in the United States Canada France Germany and Japan In this volume he has assembled the contributions of over 60 world renowned experts who are at the top of their field in the world of circuit design advancing the bank of knowledge for all who work in this exciting and burgeoning area [IEEE Transactions on Circuits and Systems](#), 2006 [Handbook of Research on Applied Optimization Methodologies in Manufacturing Systems](#) Faruk Yılmaz,

Ömer, Tüfekçi, Süleyman, 2017-11-30 Today's manufacturing systems are undergoing significant changes in the aspects of planning production execution and delivery It is imperative to stay up to date on the latest trends in optimization to efficiently create products for the market The Handbook of Research on Applied Optimization Methodologies in Manufacturing Systems is a pivotal reference source including the latest scholarly research on heuristic models for solving manufacturing and supply chain related problems Featuring exhaustive coverage on a broad range of topics such as assembly ratio car sequencing and color constraints this publication is ideally designed for practitioners seeking new comprehensive models for problem solving in manufacturing and supply chain management [Applied Optimization with MATLAB Programming](#) P. Venkataraman, 2009-03-23 Technology Engineering Mechanical Provides all the tools needed to begin solving optimization problems using MATLAB The Second Edition of Applied Optimization with MATLAB Programming

enables readers to harness all the features of MATLAB to solve optimization problems using a variety of linear and nonlinear design optimization techniques By breaking down complex mathematical concepts into simple ideas and offering plenty of easy to follow examples this text is an ideal introduction to the field Examples come from all engineering disciplines as well as science economics operations research and mathematics helping readers understand how to apply optimization techniques to solve actual problems This Second Edition has been thoroughly revised incorporating current optimization techniques as well as the improved MATLAB tools Two important new features of the text are Introduction to the scan and zoom method providing a simple effective technique that works for unconstrained constrained and global optimization problems New chapter Hybrid Mathematics An Application using examples to illustrate how optimization can develop analytical or explicit solutions to differential systems and data fitting problems Each chapter ends with a set of problems that give readers an opportunity to put their new skills into practice Almost all of the numerical techniques covered in the text are supported by MATLAB code which readers can download on the text s companion Web site www.wiley.com/go/venkat2e and use to begin solving problems on their own This text is recommended for upper level undergraduate and graduate students in all areas of engineering as well as other disciplines that use optimization techniques to solve design problems Parasitic-Aware Optimization of CMOS RF Circuits David J. Allstot, Kiyong Choi, Jinho Park, 2003-02-28 In the arena of parasitic aware design of CMOS RF circuits efforts are aimed at the realization of true single chip radios with few if any off chip components The parasitic aware RF circuit synthesis techniques described in this book effectively address critical problems in this field

Advanced Model Order Reduction Techniques in VLSI Design Sheldon Tan, Lei He, 2007-05-31 Model order reduction MOR techniques reduce the complexity of VLSI designs paving the way to higher operating speeds and smaller feature sizes This book presents a systematic introduction to and treatment of the key MOR methods employed in general linear circuits using real world examples to illustrate the advantages and disadvantages of each algorithm Following a review of traditional projection based techniques coverage progresses to more advanced MOR methods for VLSI design including HMOR passive truncated balanced realization TBR methods efficient inductance modeling via the VPEC model and structure preserving MOR techniques Where possible numerical methods are approached from the CAD engineer s perspective avoiding complex mathematics and allowing the reader to take on real design problems and develop more effective tools With practical examples and over 100 illustrations this book is suitable for researchers and graduate students of electrical and computer engineering as well as practitioners working in the VLSI design industry *ASICON 2003* Ting-Ao Tang, 2003

Computational Science - ICCS 2007 Yong Shi, Geert Dick van Albada, Jack Dongarra, Peter M.A. Sloot, 2007-07-16 Part of a four volume set this book constitutes the refereed proceedings of the 7th International Conference on Computational Science ICCS 2007 held in Beijing China in May 2007 The papers cover a large volume of topics in computational science and related areas from multiscale physics to wireless networks and from graph theory to tools for program development

Swarm Intelligence for Electric and Electronic Engineering Fornarelli, Girolamo, 2012-12-31 With growing developments in artificial intelligence and focus on swarm behaviors algorithms have been utilized in solving a variety of problems in the field of engineering This approach has been specifically suited to face the challenges in electric and electronic engineering Swarm Intelligence for Electric and Electronic Engineering provides an exchange of knowledge on the advances discoveries and improvements of swarm intelligence in electric and electronic engineering This comprehensive collection aims to bring together new swarm based algorithms as well as approaches to complex problems and various real world applications

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Performance Optimization Techniques in Analog, Mixed-Signal, and Radio-Frequency Circuit Design Fakhfakh, Mourad, Tlelo-Cuautle, Esteban, Fino, Maria Helena, 2014-10-31 Improving the performance of existing technologies has always been a focal practice in the development of computational systems However as circuitry is becoming more complex conventional techniques are becoming outdated and new research methodologies are being implemented by designers Performance Optimization Techniques in Analog Mixed Signal and Radio Frequency Circuit Design features recent advances in the engineering of integrated systems with prominence placed on methods for maximizing the functionality of these systems This book emphasizes prospective trends in the field and is an essential reference source for researchers practitioners engineers and technology designers interested in emerging research and techniques in the performance optimization of different circuit designs **2003 IEEE Conference on Electron Devices and Solid-State Circuits**, 2003 The proceedings from the 2003 IEEE Conference on Electron Devices and Solid State Circuits

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