Parallel Machines: Parallel Machine Languages

The Emergence of Hybrid Dataflow Computer Architectures

Robert A. Iannucci

foreword by Arvind



Parallel Machines Parallel Machine Languages

Jose Duato,Sudhakar Yalamanchili,Lionel Ni

Parallel Machines Parallel Machine Languages:

Parallel Machines: Parallel Machine Languages Robert A. Iannucci,1990-05-31 It is universally accepted today that parallel processing is here to stay but that software for parallel machines is still difficult to develop However there is little recognition of the fact that changes in processor architecture can significantly ease the development of software In the seventies the availability of processors that could address a large name space directly eliminated the problem of name management at one level and paved the way for the routine development of large programs Similarly today processor architectures that can facilitate cheap synchronization and provide a global address space can simplify compiler development for parallel machines If the cost of synchronization remains high the pro gramming of parallel machines will remain significantly less abstract than programming sequential machines In this monograph Bob Iannucci presents the design and analysis of an architecture that can be a better building block for parallel machines than any von Neumann processor There is another very interesting motivation behind this work It is rooted in the long and venerable history of dataflow graphs as a formalism for ex pressing parallel computation The field has bloomed since 1974 when Dennis and Misunas proposed a truly novel architecture using dataflow graphs as the parallel machine language The novelty and elegance of dataflow architectures has however also kept us from asking the real question What can dataflow architectures buy us that von Neumann ar chitectures can t In the following I explain in a round about way how Bob and I arrived at this question

Languages and Compilers for Parallel Computing Utpal Banerjee, 1993-12-08 The articles in this volume are revised versions of the best papers presented at the Fifth Workshop on Languages and Compilers for Parallel Computing held at Yale University August 1992 The previous workshops in this series were held in Santa Clara 1991 Irvine 1990 Urbana 1989 and Ithaca 1988 As in previous years a reasonable cross section of some of the best work in the field is presented The volume contains 35 papers mostly by authors working in the U S or Canada but also by authors from Austria Denmark Israel Italy Japan and the U K Parallel Computers 2 R.W Hockney, C.R Jesshope, 1988-01-01 Since the publication of the first edition parallel computing technology has gained considerable momentum A large proportion of this has come from the improvement in VLSI techniques offering one to two orders of magnitude more devices than previously possible A second contributing factor in the fast development of the subject is commercialization. The supercomputer is no longer restricted to a few well established research institutions and large companies A new computer breed combining the architectural advantages of the supercomputer with the advance of VLSI technology is now available at very attractive prices A pioneering device in this development is the transputer a VLSI processor specifically designed to operate in large concurrent systems Parallel Computers 2 Architecture Programming and Algorithms reflects the shift in emphasis of parallel computing and tracks the development of supercomputers in the years since the first edition was published It looks at large scale parallelism as found in transputer ensembles This extensively rewritten second edition includes major new sections on the transputer

and the OCCAM language The book contains specific information on the various types of machines available details of computer architecture and technologies and descriptions of programming languages and algorithms Aimed at an advanced undergraduate and postgraduate level this handbook is also useful for research workers machine designers and programmers concerned with parallel computers In addition it will serve as a guide for potential parallel computer users especially in disciplines where large amounts of computer time are regularly used 2003, חחחחח Compilers and Run-time Environments for Distributed Memory Machines J. Saltz, P. Mehrotra, 1992-02-20 Papers presented within this volume cover a wide range of topics related to programming distributed memory machines Distributed memory architectures although having the potential to supply the very high levels of performance required to support future computing needs present awkward programming problems. The major issue is to design methods which enable compilers to generate efficient distributed memory programs from relatively machine independent program specifications. This book is the compilation of papers describing a wide range of research efforts aimed at easing the task of programming distributed memory machines Past, Present, Parallel Arthur Trew, Greg Wilson, 2012-12-06 Past Present Parallel is a survey of the current state of the parallel processing industry In the early 1980s parallel computers were generally regarded as academic curiosities whose natural environment was the research laboratory Today parallelism is being used by every major computer manufacturer although in very different ways to produce increasingly powerful and cost effective machines. The first chapter introduces the basic concepts of parallel computing the subsequent chapters cover different forms of parallelism including descriptions of vector supercomputers SIMD computers shared memory multiprocessors hypercubes and transputer based machines Each section concentrates on a different manufacturer detailing its history and company profile the machines it currently produces the software environments it supports the market segment it is targetting and its future plans Supplementary chapters describe some of the companies which have been unsuccessful and discuss a number of the common software systems which have been developed to make parallel computers more usable The appendices describe the technologies which underpin parallelism Past Present Parallel is an invaluable reference work providing up to date material for commercial computer users and manufacturers and for researchers and postgraduate students with an interest in parallel **Encyclopedia of Parallel Computing** David Padua, 2011-09-08 Containing over 300 entries in an A Z format computing the Encyclopedia of Parallel Computing provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were selected written and peer reviewed by an international pool of distinguished researchers in the field The Encyclopedia is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to

other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance CUDA Programming Shane Cook, 2012-11-13 CUDA Programming offers a detailed computing distributed computing guide to CUDA with a grounding in parallel fundamentals It starts by introducing CUDA and bringing you up to speed on GPU parallelism and hardware then delving into CUDA installation Specification of Parallel Algorithms Guy E. Blelloch, K. Mani Chandy, Suresh Jagannathan, 1994 This volume contains papers presented at the DIMACS workshop on Specification of Parallel Algorithms held in May 1994 at Princeton University The goal of the workshop was to bring together some of the best researchers in parallel languages algorithms and systems to present and discuss recent developments in their areas of expertise Among the topics discussed were new specification techniques for concurrent and distributed systems behavioral and operational specification techniques new parallel language and system abstractions novel concurrent architectures and systems large scale parallel systems specification tools and environments and proof techniques for concurrent systems

PARLE '89 - Parallel Architectures and Languages Europe Eddy Odijk, Martin Rem, 1989 Since the first PARLE conference PARLE 87 attracted more than 300 participants it was considered a useful and successful forum and encouraged the organization of this second issue known as PARLE 89 The initiative for these conferences was taken by project 415 of ESPRIT the European Strategic Programme for Research and Development in Information Technology of the Commission of the European Communities Their scope covers central themes in the area of parallel architectures and languages including such topics as concurrent object oriented logic and functional programming MIMD dataflow inference and reduction machines design and verification of parallel systems VLSI WSI and RISC architectures performance evaluation memory management systolic arrays applications and special purpose architectures The four invited lectures present the state of the art and advanced developments in major research areas related to the topics of the conference Of the more than 150 submitted papers 45 were selected for presentation Furthermore the program of PARLE 89 comprises presentations on the subprojects which together constitute ESPRIT project 415 Parallel architectures based on a variety of programming styles

object oriented logic functional dataflow are represented in these overviews Algorithms and Parallel Computing Favez Gebali, 2011-04-19 There is a software gap between the hardware potential and the performance that can be attained using today s software parallel program development tools The tools need manual intervention by the programmer to parallelize the code Programming a parallel computer requires closely studying the target algorithm or application more so than in the traditional sequential programming we have all learned The programmer must be aware of the communication and data dependencies of the algorithm or application This book provides the techniques to explore the possible ways to program a parallel computer for a given application Implicit Parallel Programming in PH Rishiyur S. Nikhil, Arvind, 2001 Parallel machines are now affordable and available to many users in the form of small symmetric shared memory multiprocessors SMPs Unfortunately programming practices have not kept pace with this hardware advance. The vast majority of developers still write applications in sequential programming languages that do not exploit multiple processors The traditional approaches for adding parallelism to applications are prone to introducing new strange and difficult to eliminate bugs In this important new text the authors offer a completely different vision of the future where parallel programming is the default and sequential programming is a special case The foundation of this vision is an implicitly parallel programming language pH which is the result of two decades of research by the authors A dialect and extension of the standard nonstrict and purely functional language Haskell pH is essentially Haskell with implicitly parallel semantics pH s extensions to Haskell comprise a disciplined approach to shared parallel state so that a pH program even a beginner s program is implicitly parallel The authors have developed this text over ten years while teaching implicit parallel programming to graduate students at MIT and specialized short courses to undergraduates and software professionals in the U.S. Japan and India Data-parallel Programming on MIMD Computers Philip J. Hatcher, Michael Jay Quinn, 1991 Mathematics of Computing Parallelism Parallel Programming with MPI Peter Pacheco, 1997 Mathematics of Computing Parallelism Languages and Compilers for Parallel Computing Chua-Huang Huang, 1996-01-24 This book presents the refereed proceedings of the Eighth Annual Workshop on Languages and Compilers for Parallel Computing held in Columbus Ohio in August 1995 The 38 full revised papers presented were carefully selected for inclusion in the proceedings and reflect the state of the art of research and advanced applications in parallel languages restructuring compilers and runtime systems. The papers are organized in sections on fine grain parallelism interprocedural analysis program analysis Fortran 90 and HPF loop parallelization for HPF compilers tools and libraries loop level optimization automatic data distribution compiler models irregular computation object oriented and functional parallelism Natural and Artificial Parallel Computation Michael A. Arbib, John Alan Robinson, 1990 These eleven contributions by leaders in the fields of neuroscience artificial intelligence and cognitive science cover the phenomenon of parallelism in both natural and artificial systems from the neural architecture of the human brain to the electronic architecture of parallel computers The brain s complex neural architecture not only supports higher mental

processes such as learning perception and thought but also supervises the body s basic physiological operating system and oversees its emergency services of damage control and self repair By combining sound empirical observation with elegant theoretical modeling neuroscientists are rapidly developing a detailed and convincing account of the organization and the functioning of this natural living parallel machine At the same time computer scientists and engineers are devising imaginative parallel computing machines and the programming languages and techniques necessary to use them to create superb new experimental instruments for the study of all parallel systems Michael A Arbib is Professor of Computer Science Neurobiology and Physiology at the University of Southern California J Alan Robinson is University Professor at Syracuse University Contents Natural and Artificial Parallel Computation M A Arbib J A Robinson The Evolution of Computing R E Gomory The Nature of Parallel Programming P Brinch Hansen Toward General Purpose Parallel Computers D May Applications of Parallel Supercomputers G E Fox Cooperative Computation in Brains and Computers M A Arbib Parallel Processing in the Primate Cortex P Goldman Rakic Neural Darwinism G M Edelman G N Reeke Jr How the Brain Rewires Itself M Merzenich Memory Based Reasoning D Waltz Natural and Artificial Reasoning J A Robinson Networks Jose Duato, Sudhakar Yalamanchili, Lionel Ni, 2003 Foreword Foreword to the First Printing Preface Chapter 1 Introduction Chapter 2 Message Switching Layer Chapter 3 Deadlock Livelock and Starvation Chapter 4 Routing Algorithms Chapter 5 CollectiveCommunicationSupport Chapter 6 Fault Tolerant Routing Chapter 7 Network Architectures Chapter 8 Messaging Layer Software Chapter 9 Performance Evaluation Appendix A Formal Definitions for Deadlock Avoidance Appendix B Acronyms References Index Parallel Database Systems Pierre America, 1991-06-26 This volume presents the proceedings of a workshop on parallel database systems organized by the PRISMA Parallel Inference and Storage Machine project The invited contributions by internationally recognized experts give a thorough survey of several aspects of parallel database systems The second part of the volume gives an in depth overview of the PRISMA system This system is based on a parallel machine where the individual processors each have their own local memory and communicate with each other over a packet switched network On this machine a parallel object oriented programming language POOL X has been implemented which provides dedicated support for database systems as well as general facilities for parallel programming The POOL X system then serves as a platform for a complete relational main memory database management system which uses the parallelism of the machine to speed up significantly the execution of database queries The presentation of the PRISMA system together with the invited papers gives a broad overview of the state of the art in parallel database systems

Multicore Programming Using the ParC Language Yosi Ben-Asher,2012-05-26 Multicore Programming Using the ParC Language discusses the principles of practical parallel programming using shared memory on multicore machines It uses a simple yet powerful parallel dialect of C called ParC as the basic programming language Designed to be used in an introductory course in parallel programming and covering basic and advanced concepts of parallel programming via ParC

examples the book combines a mixture of research directions covering issues in parallel operating systems and compilation techniques relevant for shared memory and multicore machines Multicore Programming Using the ParC Language provides a firm basis for the delicate art of creating efficient parallel programs Students can exercise parallel programming using a simulation software which is portable on PC Unix multicore computers to gain experience without requiring specialist hardware Students can also help to cement their learning by completing the great many challenging and exciting exercises which accompany each chapter **Vector Models for Data-parallel Computing** Guy E. Blelloch,1990 Mathematics of Computing Parallelism

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